

GURUCE IMX6 BSP RELEASE NOTES

FOR THE WEC7 & WEC2013 EVALUATION IMAGES AND FULL SOURCE BSP

Release 1375

Wednesday, 8 February 2017

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RELEASE CONTENTS

The contents of the release packages are described below.

The latest release can always be found at this link: <https://guruce.com/imx6/latest>

The release packages:

1. iMX6 BSP Release Notes.pdf
2. GuruCE iMX6 Getting Started Guide.pdf
3. Evaluation Images

IMX6 BSP RELEASE NOTES.PDF

This document.

GURUCE IMX6 GETTING STARTED GUIDE.PDF

The GuruCE iMX6 Getting Started Guide is a step-by-step guide to help you get started with the GuruCE iMX6 Full Source BSP and the Windows Embedded Compact evaluation images so you can evaluate our BSP on various off-the-shelf iMX6 boards.

EVALUATION IMAGES

GuruCE iMX6 BSP WEC7 and WEC2013 evaluation images for various off-the-shelf iMX6 boards can be downloaded from our website at <https://guruce.com/imx6/latest>

Historic releases can be found here: <https://guruce.com/imx6-bsp-releases>.

Please note that our license does not allow the use of our evaluation images and accompanying tools for anything other than evaluating the GuruCE iMX6 BSP. In other words; the evaluation images and tools cannot be used in, or for preparation of, production devices!

SPECIAL RELEASE INSTRUCTIONS

RELEASE 1375

The evaluation kernels of this release are built with updates up to:

- WEC7
December 2016 (v7.2.2872.0)
- WEC2013
December 2016 (v8.2.6243.0)

No further special instructions for this release.

RELEASE 1100

This release changes the memory layout to support boot splash images in the reserved system partition (where eboot and NK live). Due to this memory layout change you can't simply update eboot from eboot (because the previous release eboot will think the new eboot is destined for the wrong medium). The quickest way to update the bootloader to this release is to first build a bootloader for use with the MfgTool (select 'Boot using MfgTool' in the GuruCE i.MX6 BSP catalog under Config->Bootloader) and rebuild the bootloader folder. Copy the resulting eboot.nb0 to the correct MfgTool profiles folder (make sure cfg.ini in the MfgTool folder is configured properly as well), then press [R][1] on the device running a previous release bootloader, then start MfgTool. After clicking the Start button in MfgTool, it will load the new release bootloader in the device memory and break into the bootloader menu. Now select the correct bootloader target medium (select 'Boot from NAND/SATA/SD/MMC or SPI Flash' in the GuruCE i.MX6 BSP catalog under Config->Bootloader) and rebuild the bootloader folder again. Now press [D] in the bootloader menu and upload the bootloader to the device using Platform Builder (or CELoader). When the upload is complete the device will ask you to flash the new release bootloader to the device.

Alternatively, follow the (much longer) procedure in chapter 'FLASHING THE GURUCE IMX6 EVALUATION IMAGES' in the GuruCE i.MX6 Getting Started Guide.

CHANGELIST

RELEASE 1375

- Updated GPU driver to 5.0.15.
- Improved upload, flash and boot times by ~50%.
- Added support for LZ4 compressed kernel binaries to the bootloader.
- Added support for setting ARM CPU frequency and accompanying LDO voltage.
- Now setting ARM CPU frequency according to speed grade in the bootloader.
- Added option to the catalog to disable setting the CPU frequency to the speed grade in the bootloader.
- Added options for setting default catalog selections per board.
- Added board voltages to all board header files.
- Optimized bootloader cache settings (L1/L2) to shorten boot time.
- Bootloader boot delay can now be set to 0 to shorten boot time.
- Added options to the catalog to disable L1/L2 cache optimizations in the bootloader.
- Added support for storage profiles per USDHC instance. This means that SD cards always get mounted using the same folder name, no matter in what order they are inserted, and we can now specify exactly which USDHC port to store the hive-based registry on or exactly which USDHC port to mount as root.
- Added support for NXP SDB-QP (QuadPlus) board.
- Added support for Device Solutions Opal6-QP (QuadPlus) board.
- USB OTG Client/Host auto-detection now working.
- Removed 'INFO:' from all bootloader debug output.
- Fixed boot splash image loading after a factory reset.
- Added PackBin - a tool to LZ4 compress NK.bin to NKlz4.bin.
- Added TinyBoot bootloader. This is an SPI bootloader that just resets the i.MX6 and directs it to boot from the medium selected in the catalog.
- Now always initializing SPI Flash if it is supposed to be on the board. This allows easy switching between the tinyboot.bin (SPI redirect) bootloader, to the SD/MMC, SATA, or NAND bootloader, and back again to a normal SPI bootloader.
- USB over-current signal polarities now configurable through board header files.
- Now suppressing "SoftRTC" debug message output on CE8 kernels.
- Now always building GPU tutorials (unless building a headless design).
- SD card detection using DAT3 as CD now works properly on all boards except ConnectCore6 (CC6 hardware design prevents using DAT3 as CD).
- Added PMIC selection to catalog.
- Added DA9063 driver to the BSP.
- Fixed wrong voltage calculation in PF0100 PMIC code.
- Fixed bug in memtool driver.
- Fixed I2C pin muxing in Conga-QMX6. HDMI auto-detection now working correctly.
- Fixed build error when no SPI Flash supported at all.
- Now clearing ENET MII interrupt before creating a new command frame.
- Now always initializing heap and clock structures, not just when we have display. We can now show the clock tree, even in headless bootloader builds.
- Fixed ENFC, PRE and PRG clock initializations.
- Fixed MAX_BAUD_LEAF define. ENFC now has 8 leafs, so increased this from 6 to 8.
- Improved clock tree dump output.

- Added size member to DDK_CLK_CONFIG structure to easier detect changes in this structure (and possible mismatched between bootloader and kernel).
- Setting frequency of a clock signal that is in use but has the same frequency already is now not an error any more.
- Fixed Opal6 USDHC port definitions.
- Fixed Opal6-Q UART IOMUXing.
- Fixed bug in GPIO5 interrupt handler code.
- Fixed bug in DevTree GPIO interrupt setup code.
- Added support for PCIe 100 MHz/125 MHz clock configuration.
- Now setting PCIe ref clock to 100 MHz for S/DL and to 125 MHz for D/DP/Q/QP.
- Now setting PCIe PHY configuration as per NXP recommendations.
- Now marking PCIe IRQs as shareable.
- Fixed bootshell code for CKO dividers of PLLs.
- PLL4, PLL5 and PLL6 will now be powered if you set a frequency other than 0.
- Now supporting CKO2 to CKO1 pad routing.
- Fixed potential problem with BusEnum2 registry settings.
- Implemented LDB Clock Switch Procedure for i.MX6 according to EB821.
- Now allowing conditional compile of Ethernet code with or without caching (without is default as it has better performance).
- Added missing PMIC_I2C_SPEED definition in PF0100.h.
- Removed USDHC fall-back code.
This mechanism could lead to the boot configuration written to for instance eMMC if booting a MFGTOOL bootloader with redirection to SD card but without an SD card inserted. This could then lead to unintentional corruption of for instance uboot and/or a Linux kernel in eMMC.
- Fixed bootloader SPI redirection to NAND.
- When booting from SD/MMC, boot port is now automatically determined again (so a bootloader built to boot from SD/MMC works on all USDHC ports).
- Clarified SPI/MFG redirection in the catalog.
- Implemented all clock changes for DualPlus/QuadPlus.
- Added DualPlus/QuadPlus GPR5 definitions.
- Added DualPlus/QuadPlus PRE/PRG IRQs definitions.
- Now opening PRE and PRG gates so that display works again on DualPlus/QuadPlus.
- Now ignoring PRG and PRE gates on Dual/Quad.
- Now also supporting NXP Pins Tool to generate board header files with our internal GenBoardFile tool (we still support IOMUX files too).
- Fixed wrong daisy chain value for FlexCAN on Opal-Q.
- Little improvement in JTAG scripts (not calling Core.Assign for i.MX6 Solo because it fails for solo).
- Fixed issue in ENET code where WEC7 compiler would optimize a call to ENET registers to use a 16 bit access. Now forcing 32 bit access.
- Removed precompiled header from TEST_SMP to prevent build errors when switching RAM size in the catalog.
- Now always building I2C SDK to prevent build errors when no I2C driver is selected in the catalog.
- Now always including i2csdk.dll inside the kernel image, even if no I2C driver is selected. This is needed to prevent errors when loading the display driver.
- Fixed SGT5000 Audio Driver Prefetch Abort when unloading driver.
- Fixed UDP delay issue; major rework of Ethernet and PHY drivers.
- Fixed NDIS data abort when sending 16KB UDP packets with NETIO (netio -u -b 16k <ipaddress>).

- Fixed user stack overflow problem on WEC7 when running `cl_fft`.
- Fixed bug in `config.bib` that would prevent building a SPI bootloader with a NAND kernel.
- SDK version build number now reflects the BSP revision number.
- SDKs now define `BSP_REV` symbol that reflect the BSP revision number (for compile-time determination of BSP revision in application code).
- Added BSP revision definition `BSP_REV` (for compile-time determination of BSP revision in BSP code).
- Added BSP revision information to the registry under `[HKEY_LOCAL_MACHINE\System] "BSP Revision"`.
- Refactored and cleaned up parts of the USB driver code.
- Fixed "Erase Boot Splash" button moving in CEWriter GUI.
- Fixed bug in CEWriter that occurred if the size of the bin file was exactly 512 byte aligned.
- Now cancelling all actions when "No" is clicked in the CEWriter dialog asking if you are sure you want to (re)partition the disk.
- Fixed RTC clock output on TS4900.
- Now enabling 24MHz FPGA clock output on TS4900.
- Updated TS4900-Q DDR3 script.
- Fixed OpenGL XAML render resize rounding problem.
- Fixed OpenGL XAML render plugin color conversion problem (red/blue channel swap).
- Fixed issue with M41T8x RTC; when oscillator failure bit is set you can only reset this flag after starting the oscillator and letting it run for at least 4 seconds.
- Fixed `SCLKPOL/SCLKPHA` bug in ECSPi header; values now match the RM.
- Updated ECSPi driver code to work around TC interrupt silicon bug.
- Fixed boot splash image slant if bitmap width was not dividable by 4.
- Added backlight driver to evaluation kernel OS Design.
- Fixed problem where the GuruCE WEC7 SDK was not listed in Visual Studio when creating a native application.

RELEASE 1100

- Updated Vivante GPU driver to v5.0.11.p8.3
- Multi-monitor functionality now working properly for all resolutions on i.MX6 Solo and DualLite processors
- Boot splash images can now be stored in the system partition (next to the bootloader and kernel images). No need to have a FAT12/16/32 formatted partition anymore
- Background color of boot splash screen now determined by color of bottom right pixel of the boot splash image
- Fixed boot splash slant on non-32-bit aligned resolutions
- Updated CEWriter tool to support flashing boot splash images
- Added support for external drives (like SATA connected through USB) to CEWriter
- Updated documentation to include instructions for flashing boot splash images using CEloader and CEWriter
- Fixed flickering mouse cursor and bad performance when drawing under the mouse cursor.
- Added full support for non-hardware acceleration setting through `BSP_DISPLAY_EMULATION` environment variable
- Removed unused C2D functionality from IPU driver code
- OpenGL XAML render plugin now working properly
- Cloned and fixed bugs in XamlEffects code
- Added catalog option for display without GPU

- Fixed missing GPU headers (OpenGL, OpenVG, etc) in GuruCE iMX6 SDK for Windows Embedded Compact 7
- Clarified procedure in Getting Started Guide for updating the kernel and bootloader images if the GuruCE bootloader is already running on the device
- Added support for external RTCs (Real Time Clock chips). Currently supported (and runtime auto-detected) chips are ISL1208, ISL2020, M41T0, M41T8x and RV4162
- Added catalog option to determine the frequency of the internal oscillator used for the SOC RTC in case no external 32.768 kHz crystal is attached to the RTC_XTAL pins of the i.MX6. Calibration is needed because the internal ring oscillator can range anywhere from 14 to 66 kHz, so the SOC RTC may run way to slow or way to fast if assumptions are made about the frequency of the internal ring oscillator
- Bootloader code now consistent with CE VFP setup
- Added bootshell option for directing clocks to CKO1/2 pads
- Added support for NAND to bootloader and kernel
- Added all source locations for ENET to the catalog item
- Fixed problem in SMP FPV code causing glitches when doing floating point calculations on multiple threads running on multiple cores
- Fixed bug in cloned GPE 'sources' file
- Clarification in config.bib regarding shared video memory
- Removed IMGNOKITL dependency from ConMgr subproject
- Added simple reset application
- Added support for extended (numpad) characters to CLI code.
- Added SYSGEN_SERVICES and SYSGEN_CMD dependency for CLI component
- Added SYSGEN_FATFS dependency for AutoFormat component
- Added some more definitions to IOMUX header files
- Added PCIe core reset code so PCIe driver works on DualPlus/QuadPlus
- Worked around quirks of the PCIe module so enumeration works correctly in all cases in multi-core environments
- Changed condition for enabling L2 cache double line fill feature to depend on L2C-310 revision. The i.MX6 Solo/DualLite and DualPlus/QuadPlus now all have r3p2, so for those processors the double line fill feature is now enabled, resulting in much better cache behaviour (and better realtime performance on those processors)
- Fixed and worked around many more bugs and quirks of the ECSPI module. ECSPI driver now working properly at high speeds (tested up to 30 MHz which is the maximum supported ECSPI frequency you can set within limits)
- Fixed problem with setting time zone
- Fixed .NET CF JIT debugger crash when debugging large managed applications
- Fixed multi-cast bug in Ethernet code
- Fixed PHY cable attach/detach status update logic
- Fixed bug in GPT driver code
- Fixed bug in OALStall implementation
- Fixed missing code in GetGpioConfig function of GPIO driver
- Fixed GPIO setting for USER_LEDs in ConnectCore6 board header file
- Fixed bug in CAN transceiver power signal setup on ConnectCore6
- Fixed GPIO USB Host power signal to fix USB issue on Toradex Iris boards
- Improved clocking code to now properly control reference counts of static divider root clocks

- Replaced use of ECSPi enums in board header files (the pre-processor doesn't resolve enums properly)
- Fixed MfgTool USB PID for Opal6-Q boards
- Fixed profile folders in MfgTool cfg.ini for Opal6-DL/Q
- Moved clocking code to more logical place in the BSP tree
- Further cleaned up and refactor of large parts of the interrupt code
- Further clean-up and re-structure of BSP code and tree

RELEASE 954

- Added support for true multi-monitor setups in Windows CE (extended desktop with GDI on both displays)
- Added support for multi-display boot splash screens, with virtually flicker-free transition to CE desktop
- Added support for dynamic selection of multi-display configuration in the bootloader with immediate activation of the selected displays
- Added CEWriter tool; a utility that can write a bootloader and kernel binary directly to a SATA drive or SD card from desktop Windows, and very fast as well!
- Added support for PCIe enumeration over bridges and switches
- Added support for Command Line Interface (cmd.exe) over UART
- Added support for iMX6 Dual Plus and Quad Plus processors
- Added support for VMINI/VBRIDGE network adapter while debugging over KITL
- Added support for very large SATA disks
- Added support for SD/eMMC 8-bit mode
- Added support for eMMC 4.4 DDR mode
- Added support for the Technologic Solutions TS-4900 Solo & Quad board
- Added support for the Device Solutions Opal6 Quad board
- Added support for the Toradex Colibri DualLite board
- Added support for boards with 256MB total memory
- Added support for the N25Q SPI NOR Flash
- Added support for the SMSC 9500 USB network adapter
- Added support for the LAN8720 PHY
- Added support for the KSZ8041 PHY
- Added support for the Freescale/NXP PF0100 PMIC
- Added support for FTDI USB to Serial devices
- Added ability to select the Ethernet interface (MII/RMII/RGMII) in the board header file
- Added ability to set UART type (DCE/DTE) in the board header file
- Added ability to set the default display backlight level in the board header file
- Added "Erase SPI Flash" functionality to the bootshell
- Added IPU Display Settings control panel applet that can be used to set display gamma, rotation and allows dynamic resolution changes
- Added "cleanbsp.cmd" in BSP folder root (this deletes all the build generated files in the BSP)
- Reduced system partition size for kernel and bootloader
- USB disk now called "USBdisk" instead of "Harddisk" (to allow to distinguish between SATA "Harddisk")
- Performance improvements and thread-safety fixes to SDBus
- Bootloader now disables OTG connection when MfgTool process has finished

- Bootloader TTB setup code now supporting scattered pages inside a section
- Bootloader now omits display selection menu when built for headless device
- Increased allowed bootloader size from 512K to 1MB (to allow flashing of debug bootloaders with code optimization off; for easy JTAG debugging)
- Further optimizations of L2 cache operations to allow better real-time behaviour in the kernel
- L2 cache can now be configured in normal or write-through mode or completely disabled in the bootloader
- Implemented fix for ERR006358 in ENET code
- Added catalog option to disable Gigabit connections for Ethernet (currently this is forced until we find a workaround for the bad performance on Gigabit Ethernet)
- Updated JTAG scripts and improved 'private' symbol path resolve script
- Updated Vivante GPU driver to v5.0.p8.0
- Fixed edge-case USDHC data corruption on WEC7
- Fixed bug in L2 cache code
- Fixed bug in timer interrupt handling code
- Fixed bug in VPU code
- Fixed CPU idle count calculation code
- Fixed IPU rotation issue at boot (no more crash when rotation is enabled at boot)
- Fixed disappearing mouse cursor when screen is rotated
- Fixed GPU hang on boards with 2 GB of memory
- Fixed GPU stutter at certain time intervals
- Fixed stack problem in OpenCL tutorials
- Fixed chromatic aberration on iMX6 S/DL HDMI output
- Fixed backlight driver code and Advanced Backlight control panel applet code
- Improved HDMI 1920x1080 timings so that more monitors recognize this resolution
- Improved DDR3 scripts according to latest recommendations from NXP
- Improved Ethernet code
- Improved and consolidated PHY initialization code
- Improved TEMPMON driver code
- Improved UART driver code
- Added SIP (software input panels) to the evaluation kernels
- Added shell.exe to the evaluation kernels so that CE Shell commands can be used by typing "shell -c <command>"
- Added CPUload tool to the evaluation kernels (to show per-CPU load)
- Added Rotate tool to the evaluation kernels (to rotate the screen)
- Further clean-up and re-structure of BSP code and tree

RELEASE 550

- Optimized L2 cache code for real-time behaviour and added catalog items for easy configuration of cache performance options
- Added full support for L1/L2 exclusive cache
- Decoupled cache optimizations from bootloader
- Added performance metric collection code to the L2 cache routines
- Consolidated all low-level cache code into one folder (cleaner BSP structure)
- Optimized some ARM errata implementations
- Added DDK support for easy setting of memory cache attributes
- Added DDK support for easy setting of CKO pin muxing
- Added support for running Qt 5.5 on the evaluation kernels
- Added support for Freescale SDP-SDL and SDP-Q boards
- Added support for Boundary Devices Nitrogen6_VM board
- Added embWise AR6K SDIO evaluation WIFI drivers for WEC7 and WEC2013
- Bootloader now supports selecting LVDS0 or LVDS1 for display output
- Bootloader now turning all configured PWM outputs off (so display backlights are turned off as early as possible in the boot process)
- Changed bootloader factory defaults to use static IP instead of DHCP
- Improved bootloader SPI Flash code
- Added command history support to the bootloader bootshell (use up/down arrows to cycle through historic commands)
- Added DDR performance test to bootloader bootshell cache tests
- Added reset redirect options to the bootloader (allowing, for instance, Linux dual-boot, boot to USB without using the switches, boot to SATA on a device with fuses set to just boot from SPI, etc)
- Added support for parallel RGB LCD pin muxing and control signals
- Added many more register definitions
- More consistent implementation of FIXUPVARS
- All drivers now have their own bib and reg file (cleaner BSP structure)
- Added support for RS485 tight CTS toggling in serial driver code
- Added support for Dr. Watson
- Added support for multi-core debugging
- Added TEMPMON driver
- Added NETIO benchmark code
- Improved the memtool driver, SDK DLL and application
- Cloned and improved FTPD (FTP server) code
- Made all GPU tutorials use the same style parameters
- Added cl_math and cl_loadstore GPU tutorials
- Improved JTAG scripts
- Further improvements to BSP structure, clean code and overall code quality

RELEASE 474

- Increased BSP performance: 4.75 times faster than previous release!
- Added support for HDMI automatic display detection and hot-plug, including resolution changes in CE
- Fixed HDMI purple line
- Added bootloader option for selecting display output (LCD, LVDS or HDMI)
- Added bootloader option to enable or disable L2 cache
- Added support for DMA on all UARTs
- Upgraded to latest Vivante GPU GALCORE driver v5.0.11(25762)
- Added all of the Vivante OpenGL ES 1.1, 2.0, OpenVG and OpenCL tutorial examples
- Now asynchronously loading drivers for faster boot
- Added support for 2 GB option on SABRE-Lite and Nitrogen6X
- Improved JTAG scripts and added some functionality (like multi-core and DLL debugging scripts)
- Some clean-up and restructuring of code and registry files

RELEASE 406

- Added initial support for Boundary Devices Nitrogen6X (still some issues to resolve, see “Known Issues and Limitations” below)
- Fixed USB OTG dependencies in the catalog
- Improved real-time performance of the kernel
- Added option for enabling or disabling the watchdog to the bootloader menu
- Renamed ‘Format’ to ‘Erase’ in the bootloader menu to better describe the functionality
- Added simple memory test to the bootloader bootshell
- Added more information dumps to the bootloader bootshell
- Changed naming of USDHC instances in the bootloader
- Fixed Ethernet problem that occurred on some Sabre Lite boards
- Fixed UART DMA problem
- Fixed ILTiming OAL implementation

RELEASE 363

- Added PCIe support
- Bootloader and CE kernel now print revision information
- Clarified naming of some catalog items
- DISPLAY_ENABLE signal now configurable through board header file
- Backlight PWM now configurable through board header file
- Audio I2C channel now configurable through board header file
- Fixed power manager timeout settings in registry
- Added XML runtime, ATL, Application Builder Debugging Support, Remote Tools Support, Connection Manager Support, OpenGL XAML plugin and VPU driver to demo kernel images
- Added full support for Device Solutions Opal6
- Added full support for Digi ConnectCore6
- Added support for Dialog DA9063 PMIC
- Added support for KSZ9031 Ethernet PHY
- Added support for WSVGA 1024x600 LVDS panels
- Added support for Fusion Touch Controller (F10A-0102)
- Cleaned up ConMgr subprojects
- All UART code now using Device Tree for IO Mux setup

RELEASE 299

- Implemented new ARM erratum #845369 "Under very rare timing circumstances, transitioning into streaming mode might create a data corruption"
- Added sysgen_ethernet in WEC7 OS Design so the network interface component is now visible in the WEC7 kernel
- Slowed down ENET MII interface speed to allow all boards to communicate properly with the Ethernet PHY during PHY configuration
- Now not setting BSP_USDHCn_SD by default in sources.cmn (this fixes wrong display in the bootloader menu [M], [A])

RELEASE 282

- Initial release

KNOWN ISSUES AND LIMITATIONS

Issue ID	Category	Description	Workaround
385	USDHC	On Digi's ConnecCore6 USDHC2 card removal/insertion does not work. Hardware problem: can't fix this in software.	Insert USDHC2 card at start up and don't remove the card.
394	WIFI	No support for WIFI on Boundary Devices Nitrogen6X (no CE WIFI driver available).	None.
423	Audio	No audio driver for the Realtek ALC888 codec on the Conga QKIT-ARM board.	None.
426	Audio	No audio driver for WM8962 on Freescale SDP boards yet (driver development in progress).	None.
443	Touch	Touch doesn't work correctly in multi-monitor mode.	None.
461	Touch	HSD100PXN1 Touch not working properly	None.
465	GPU	GPU driver v5.0.15 does not support the new prefetch & resolve functionality in the Dual+/Quad+ processors.	None.
495	GPU	Tearing and loss of HDMI 1920x1080 display timing when running GPU tutorials on NXP SDB-QuadPlus board.	None.
506	Bootloader	Setting CPU frequency to speed grade causes boot problems on TS4900-Q.	Set BSP_BL_NOSPEEDGRADE option in the catalog.
507	Bootloader	Setting CPU frequency to speed grade causes boot problems on ConnectCore6 boards.	Set BSP_BL_NOSPEEDGRADE option in the catalog.
516	Backlight	On Digi's ConnectCore6 backlight control does not work.	None.
533	USB	USB OTG in host mode can't work on Colibri due to USB_OTG_ID pin not connected.	Don't use OTG port in host mode, just use USB Host port.
535	USB	USB OTG in host mode causes reset on Nitrogen6_VM.	Don't use OTG port in host mode, just use USB Host port.

GuruCE prioritizes work according to customer requests.

If you find any other bugs or components not working correctly, please send a bug report to bugs@guruce.com using the template on the next page.

Revision: GuruCE iMX6 BSP [rXXX]

Component: [USB/I2C/etc.]

Description of current behaviour:

Description of expected behaviour:

Steps to reproduce:

Additional information:

SUPPORT

GuruCE offers various support options. Please visit <http://guruce.com/support> for more information.

GuruCE APAC/NZ

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